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**AMENDMENTS TO THE SPECIFICATION:**

**Please amend paragraphs 1-3 on page 16 as follows:**

Figure 6 illustrates a system 600 for analyzing power distribution according to a preferred embodiment of the present invention. The inventive system 600 includes a chip design device 610 into which precharacterized cell data is input. The chip design device 610 uses this data to logically and physically design the chip, and output physical design means to ~~an a power grid~~ extracting device 620.

As noted above, the ~~power grid~~ extracting device 620 extracts the parasitic resistances, capacitances and inductances from the physical design to out put an extracted netlist.

The inventive system 600 inputs the extracted netlist data into the static timing analysis tool 630. As explained above, the static timing analysis tool calculates circuit timing arrival windows within a clock cycle of the chip which the inventive system then divides into a plurality of time periods to generate switching information including whether a circuit is capable of switching in each time period. The inventive system 600 then inputs the ~~physical design data and~~ the extracted power grid ~~and uses the switching information to generate current waveform data for each cell which may be input~~ to a power distribution analysis tool 640 to analyze the power distribution of the chip.